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Chapter 4 CMOS Cascode Amplifiers 4.1 Introduction A single stage CMOS amplifier cannot give desired dc voltage gain, output resistance and transconductance. The voltage gain can be made to attain higher value by using active load like current source. A single stage CS amplifier can offer infinite input resistance,

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The CS r_M is R_S in series with the drain resistance, referred to the source. Drain resistance is r_{o1} in series with the CG drain circuit referred to its source, or $(r_{o2} + R_L) / (\beta + 1)$. When these resistances are referred to the CS source, the denominator of (4.21), r_M , results.

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4 Gain-Boosted Telescopic Cascode Op Amp V_{DD} V_{OUT} V_{B2} V_{B3} V_{S1} V_{S2} V_{B5} M_1 M_2 M_3 M_4 M_5 M_7 M_6 M_8 Advantages: Significant increase in dc gain Limitations: \square Signal swing ($4V_{SAT} + V_T$ between V_{DD} and V_{SS}) \square Reduction in GB power efficiency - some current required to bias \square A_{\square} amplifiers ...

Lecture 10: Folded-Cascode Amplifiers Current Mirror Op Amps

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